

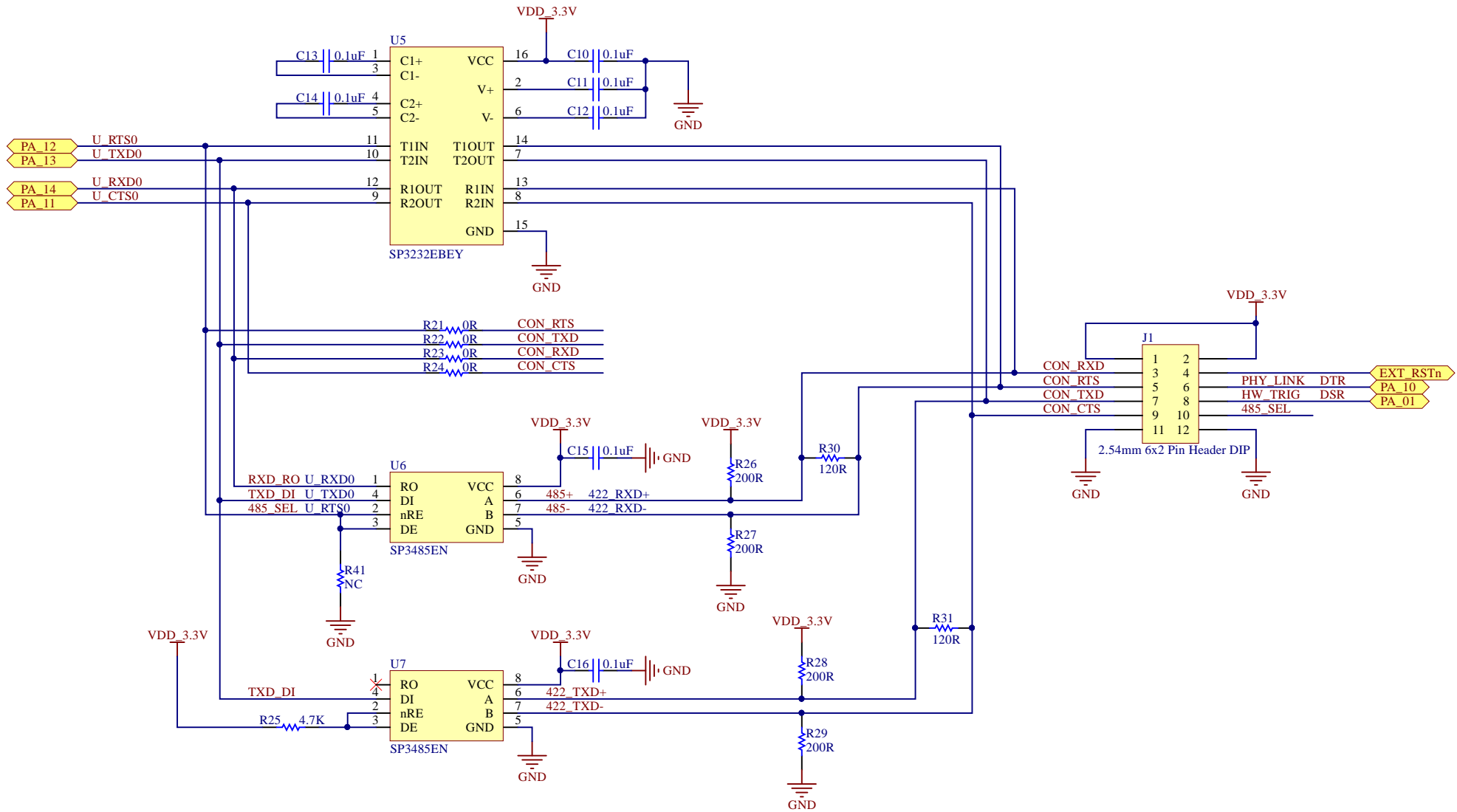
As close to RJ-45 pin R3 as possible

<Ethernet line routing>
Do not position to other elements(R,L,C) to RJ-45 below
There should be no other signal under the differential signal.

PA_10 is with PHY_LINK Status function (output signal)
PA_01_with HW_Trigger and TCP connection pin function (output signal)

*** Caution**
PA_09 pin must be connected.
This pin is PHY LINK check pin(input), and this pin checks PHY link from peer

Title WIZ750SR_W7500P		WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea		
Size: A4	Number:	Revision:V1.1		
Date: 2018-08-08	Sheet 1 of 3	Team: Team CS	Drawn By: Edward	



Mode	R21~24	U5	U6	U6,7
TTL	Mount			
RS232		Mount		
RS422			Mount	
RS485				Mount

The user must select one of three modes.

The TTL mode mounts R21 to R24 resistor.

RS232 mode mounts U5, C10 - C14.

The RS422 / 485 mode mounts U6,7, C15,C16, R25~R31.

Title *WIZ750SR_Transceiver*

Size: A4

Number:

Revision:V1.1

Date: 2018-08-08

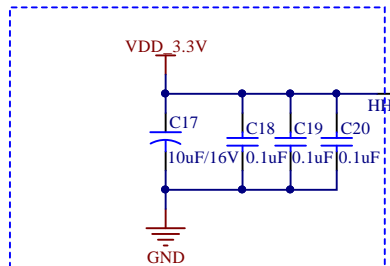
Sheet2 of 3

Team: Team CS

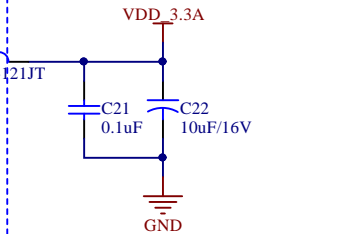
Drawn By: Edward

WIZnet Co., Ltd.
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South Korea

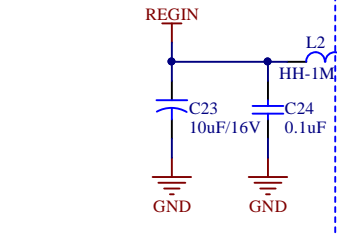




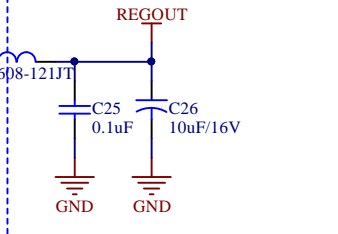
As close to W7500P(page1) pin10,18,61 as possible



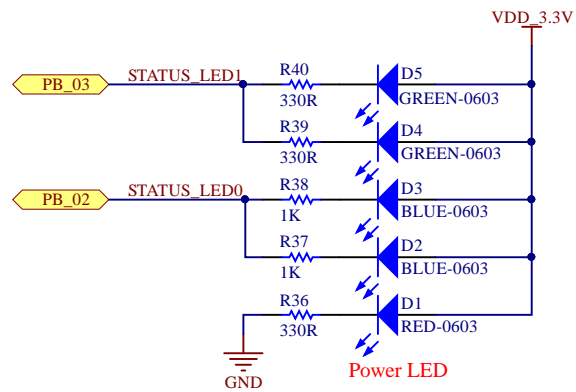
As close to W7500P(page1) pin40 as possible



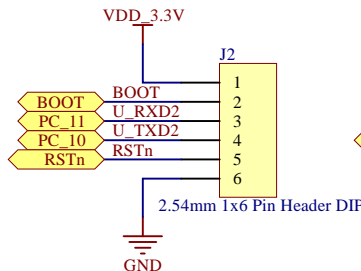
As close to W7500P(page1) pin13 as possible



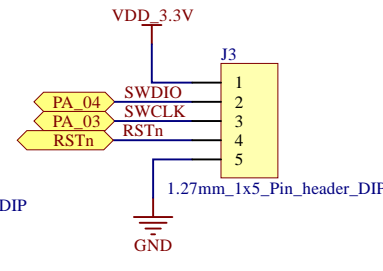
As close to W7500P(page1) pin36 as possible



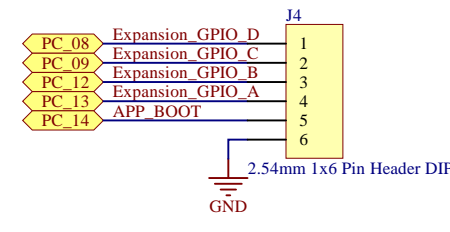
Indicate LED



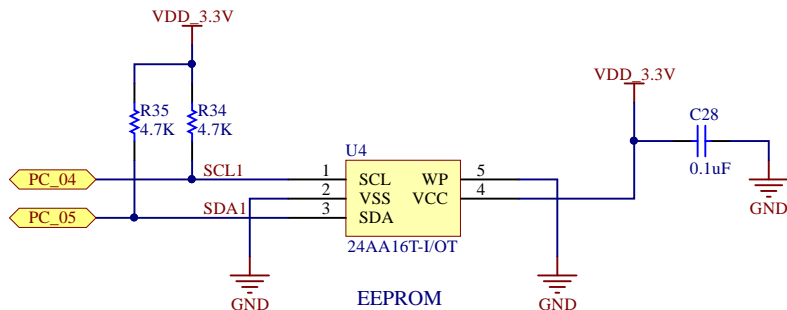
ISP Connector



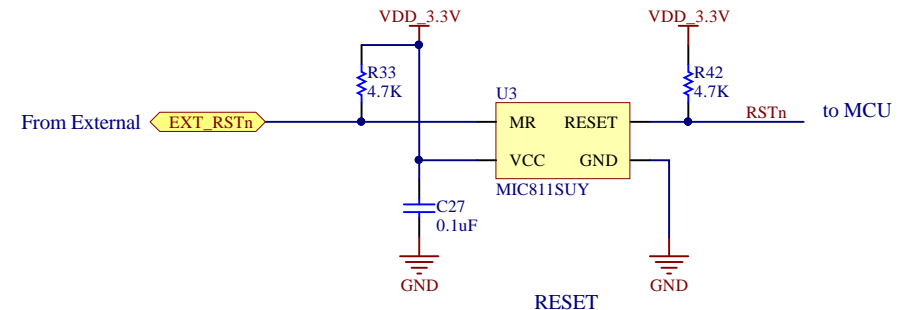
JTAG Connector



Expansion GPIO + App boot Connector(Output)



NM(Not Mount)



RESET

Title WIZ750SR_ETC		WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea		
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Date: 2018-08-08	Sheet 3 of 3			
Team: Team CS	Drawn By: Edward			