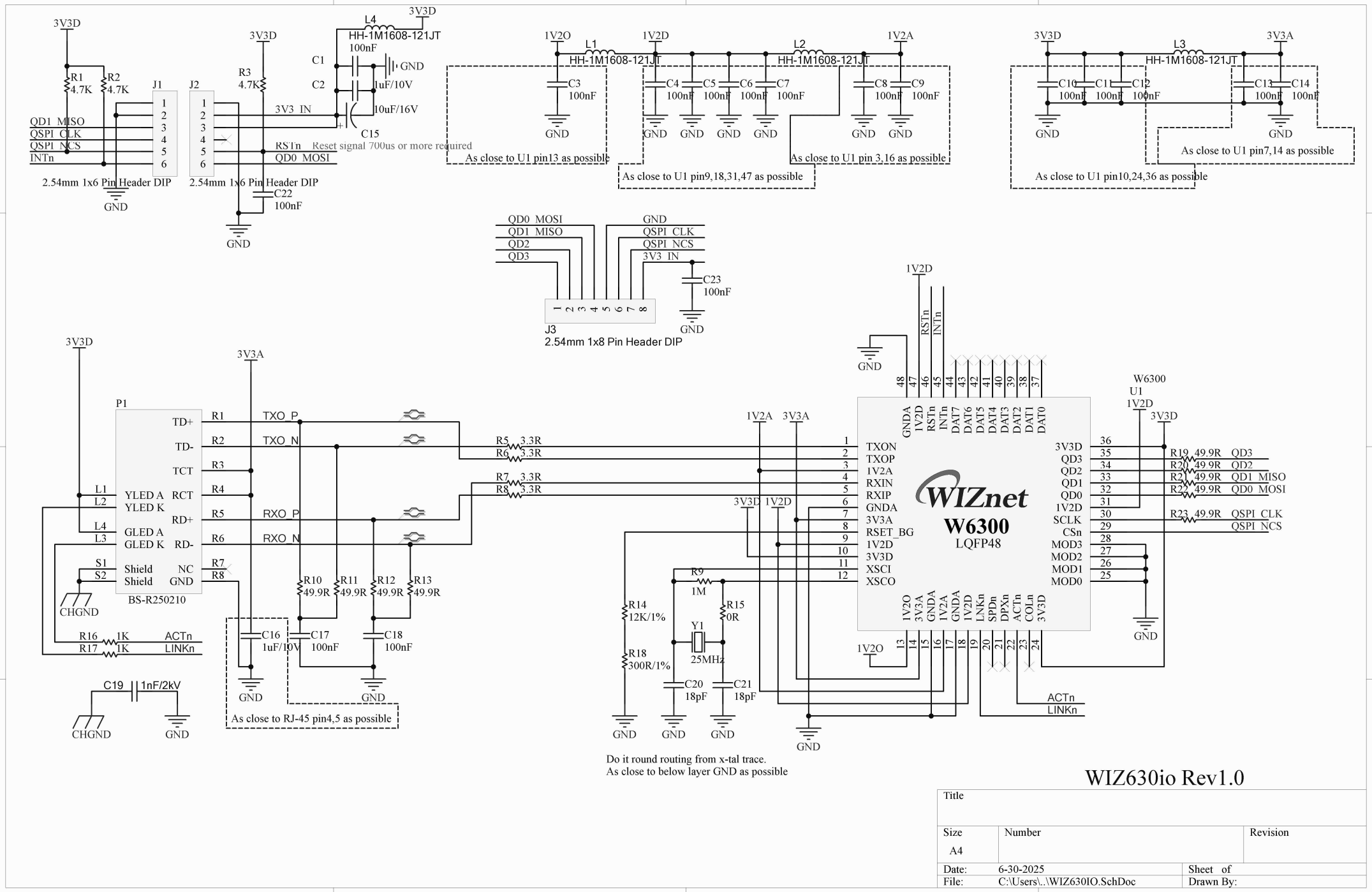
3V3D
R1 4.7K
R2 4.7K
J1
QD1 MISO
QSPI CLK
QSPI NCS
INTn
2.54mm 1x6 Pin Header DIP
GND
J2
R3 4.7K
3V3 IN
RSTn
QD0 MOSI
Reset signal 700us or more required
C22 100nF
L4 HH-1M1608-121JT
C1 100nF
C2 1uF/10V
10uF/16V
C15
1V2O
L1 HH-1M1608-121JT
1V2D
L2 HH-1M1608-121JT
1V2A
C3 100nF
C4 100nF
C5 100nF
C6 100nF
C7 100nF
C8 100nF
C9 100nF
As close to U1 pin13 as possible
As close to U1 pin9,18,31,47 as possible
As close to U1 pin 3,16 as possible
3V3A
L3 HH-1M1608-121JT
C10 100nF
C11 100nF
C12 100nF
C13 100nF
C14 100nF
As close to U1 pin7,14 as possible
As close to U1 pin10,24,36 as possible
QD0 MOSI
QD1 MISO
QD2
QD3
QSPI CLK
QSPI NCS
3V3 IN
C23 100nF
J3
2.54mm 1x8 Pin Header DIP
P1
TD+
TD-
TCT
YLED A
YLED K
RCT
GLED A
GLED K
RD+
RD-
Shield
NC
GND
BS-R250210
TXO_P
TXO_N
RXO_P
RXO_N
R5 3.3R
R6 3.3R
R7 3.3R
R8 3.3R
R10 49.9R
R11 49.9R
R12 49.9R
R13 49.9R
CHGND
R16 1K ACTn
R17 1K LINKn
C16 1uF/10V
C17 100nF
C18 100nF
C19 1nF/2kV
As close to RJ-45 pin4,5 as possible
W6300
U1
1V2D
RSTn
INTn
GNDA
DAT7
DAT6
DAT5
DAT4
DAT3
DAT2
DAT1
DAT0
TXON
TXOP
RXIN
RXIP
RSET_BG
XSCI
XSCO
WIZnet
W6300
LQFP48
SCLK
CSn
MOD3
MOD2
MOD1
MOD0
R19 49.9R QD3
R20 49.9R QD2
R21 49.9R QD1 MISO
R22 49.9R QD0 MOSI
R23 49.9R QSPI CLK
QSPI NCS
R9 1M
R14 12K/1%
R15 0R
Y1 25MHz
R18 300R/1%
C20 18pF
C21 18pF
LNKn
SPDn
DPXn
ACTn
COLn
Do it round routing from x-tal trace.
As close to below layer GND as possible
WIZ630io Rev1.0
Title
Size A4
Number
Revision
Date: 6-30-2025
Sheet of
File: C:\Users\..\WIZ630IO.SchDoc
Drawn By: